

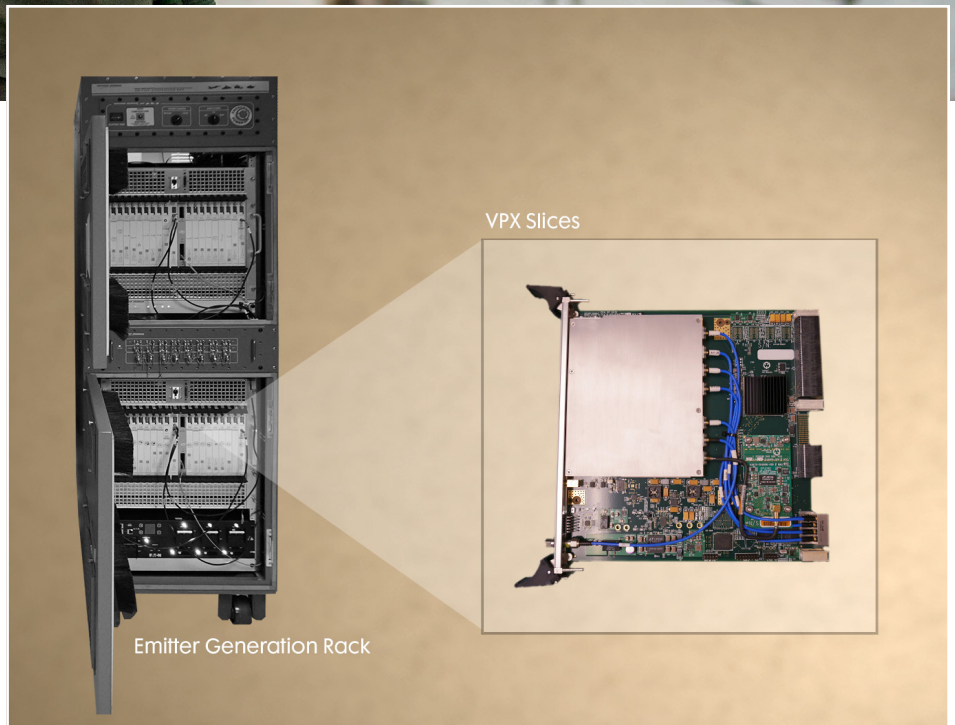


## CEESIM-VPX

New RF subsystem design/package, CEESIM-VPX builds on existing CEESIM capabilities.

### Features

- VPX Architecture
- Industry Leading RF Performance
- Plug N Play Auto Configuration
- Scalable Solution
- Simple Expansion in the Field
- User Reconfigurable





Parameter	CEESIM-VPX
<b>Frequency Control</b>	Direct Digital Synthesis
<b>Tuning Time</b>	0.5 usec
<b>Frequency Resolution</b> 20 MHz to 40 GHz	1 Hz
<b>Frequency Accuracy</b>	±2 Hz
<b>Phase Noise @ SUT ports for 20 MHz-18 GHz</b> 1 kHz offset 10 kHz offset 100 kHz offset 1 MHz offset 10 MHz Offset	-95 dBc/Hz -110 dBc/Hz -120 dBc/Hz -130 dBc/Hz -137 dBc/Hz
<b>Phase Noise @ SUT ports for 18-40 GHz</b> 1 kHz offset 10 kHz offset 100 kHz offset 1 MHz offset 10 MHz offset	≤-92 dBc/Hz ≤-107 dBc/Hz ≤-117 dBc/Hz ≤-125 dBc/Hz ≤-135 dBc/Hz
<b>Broadband Noise @ CEESIM Output ports</b> 20 MHz-40GHz	-85 dBc/MHz (typ)

Parameter	CEESIM-VPX
<b>Spurious @ CEESIM Output ports</b> 20MHz-40 GHz	-70 dBc (typ)
<b>FMOP Deviation</b>	±500 MHz
<b>FMOP Accuracy</b>	±1%
<b>FMOP Unlock Offset</b>	0 Hz
<b>PMOP Resolution</b>	1 degree
<b>PMOP Accuracy</b>	±2 degrees
<b>Maximum MOP Sample Rate</b>	1280 MSPS
<b>MOP Pattern Playback Capacity</b> <b>Memory Stream I/Q from External Data Source</b>	2 GB Yes
<b>Preserve MOP pattern with TDOA</b>	Yes
<b>Phase Coherency</b>	All emitters
<b>Required Emitter Calibrations</b>	None

**For more information, please contact:**

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